

(1)

Ex: Write 1msec delay segment for 4 MHz oscillator.

Sln: For 1msec we need

$$\frac{1 \text{ msec}}{1 \text{ sec}} = 1000 \text{ cycles}$$

$$1 \text{ Cycle for } 4 \text{ MHz OSC} = 4 \cdot \frac{1}{4 \text{ MHz}} \\ = 1 \mu \text{ sec.}$$

movlw 250, → 1 Cycle  
movwf Counter → 1 Cycle

Loop

decfsz Counter, F, 258 × 1 + 2 cycles

goto loop, 258 × 2 cycles

$$258 \times 1 + 2 + 258 \times 2 = 748 \text{ cycles}$$

$$748 + 2 = 750 \text{ cycles in total}$$

If we put one NOP command before

decfsz Counter, F command the

$$\text{Total cycles will be } 750 + 250 = 1000 \text{ cycles}$$

(2)

Hence 1ms delay segment  
For 4MHz oscillator is as follows

```

Counter equ 0x0C
        movlw 250;
        movwf Counter;

Loop    NOP; No-Operation
        decfsz Counter,F; Decrement Counter
        goto Loop; Skip if Zero

```

↓ 1ms delay program segment.

Ex<sup>o</sup>

Write a 100msec delay segment

```

Counter-Outer equ 0x0D
        movlw N; N can be any integer non-negative
        movwf Counter-Outer

```

```

Loop-Outer    1-ms-Delay-Segment; ← Put here
               decfsz Counter-Outer; 1ms
               goto Loop-Outer; Delay
                                   Segment

```

3

Compute the total number of cycles  
in previous example for  $N=100$  case

Counter-Outer    `movl 0x0D`  
                           `movl N`                     $\rightarrow$  1 Cycle  
                           `movl Counter-Outer`  $\rightarrow$  1 Cycle

loop-Outer            1ms-Delay-Segment  $\rightarrow N \times 1001$  Cycles  
                           `decfsz Counter-Outer`  $\rightarrow (N-1) \times 1 + 2$   
                           `goto Loop-Outer`     $(N-1) \times 2$

Total Number of Cycles

$$1 + 1 + N \times 1001 + N + 1 + 2N - 2$$

$$= N \times 1004 + 1$$

For  $N=99$             Total Number  
                           of Cycles = 99397

i.e., delay amount is 99397  $\mu$ sec

$$\approx 99.5 \text{ msec}$$

$$\approx 100 \text{ msec}$$

For more accurate computation

6 NOP operation can be inserted as follows

(4)

Counter-Outer equ 0x0D

movlw 200

movwf Counter-Outer

Loop-Outer

nop nop nop nop nop nop; → 6  
1ms-Delay-Segment NOP

decfsz Counter-Outer;

goto Loop-Outer

Comments  
are  
inserted

Total Number of Cycles = 99857 + 600

=

NOP  
cycles  
6x100

= 99857 μsec

≈ 100msec

=====

↓ More  
Accurate  
computation

(5)

Ex 3

1sec Delay Segment for 4m Hz Oscillator.

Counter-Outer equ 0x00

movlw N, → 1 cycle

movwf Counter-Outer, → 1 cycle

Loop Outer

1ms-Delay-Segment → 100<sup>xN</sup> cycles

decfsz Counter-Outer → (N-1) × 1 + 2 cycles

goto Loop-Outer. → (N-1) × 2 cycles

$$\begin{aligned}
 \text{Total number of cycles} &= 1 + 1 + 100N + N - 1 + 2N - 2 \\
 &= 2 + 100N + 3N - 3 \\
 &= 1004N - 1
 \end{aligned}$$

For 1sec delay you need

1000000 cycles

Maximum N = 255

$$1004 \times 255 - 1 < 1000000$$

Hence we need another outer loop

(6)

Hence we can write the following  
Segment for 1sec delay

```
Counter-Outer-Most equ 0x0
```

```
movl 10,
```

```
movw Counter-Outer-Most
```

```
Loop-0-Most
```

```
100ms-delay-segment
```

```
decfsz Counter-Outer-Most, F
```

```
goto Loop-0-Most
```

The above segment creates almost  
1sec delay

1sec-Delay For 4MHz Oscillator. (7)

Hence

```
Counter 2 equ 0x0E
Counter 1 equ 0x0D
Counter 0 equ 0x0C
```

```
movlw 10
movwf Counter 2
```

```
movlw 100;
movwf Counter 1
```

```
nop
nop
nop
nop
nop
nop
```

```
movlw 250;
movwf Counter 0
```

```
nop
decfsz Counter 0, F
goto Loop 0
```

```
goto Loop 1
```

```
goto Loop 2
```

Loop 2

Loop 1

Loop 1

Loop 0

1ms delay

100ms delay

1sec delay

# Subroutines

8

Subroutines represents a set of instructions beginning with a label and ending with the instruction return or retlw.

Subroutines are similar to those "functions" used in C++ or Java programming.

```
Subroutine-Name, Label
    set of instructions
    "
    "
    return or retlw
```

When a subroutine is called, the content of PC which holds the address of the next instruction to be executed is saved into stack registers.

A jump to subroutine segment is performed. After completion of subroutine segment PC (Program counter) is loaded back to its original content and program execution continues.